DECLARATION FOR PATENT APPLICATION

As a below named inventor, I hereby declare that:

P.O. Address: Nishi 3-2-62 Kunitachi-shi, Tokyo, Japan 186-0005

My residence, post office address and citizenship are as stated below next to my name.

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled INTEGRATED CIRCUITS HAVING POST-SILICON ADJUSTMENT CONTROL the specification of which

(check one) X is attached hereto.		
was filed on		as
	erial No.	
and was amen	ded on	, if applicable.
I hereby state that I have reviewed and un claims, as amended by any amendment re		the above identified specification, including the
I acknowledge the duty to disclose information Federal Regulations §1.56.	nation which is material to	o patentability as defined in Title 37, Code of
	w and have also identified	es Code, §119 of any foreign application(s) for below any foreign application for patent or ion on which priority is claimed.
Prior Foreign Application(s)		Priority Claimed
(Number) (Countr	y) (Day/Month	Yes_ No_X
(rumber) (Countr	y) (Day/Month	rear riled)
application in the manner provided by the duty to disclose material information as d	e first paragraph of Title 3 lefined in Title 37, Code of action and the national or I	cation is not disclosed in the prior United States 5, United States Code, §112, I acknowledge the of Federal Regulations, §1.56(a) which occurred PCT international filing date of this application: Pending s-patented, pending, abandoned)
I hereby appoint the following attorney to p Trademark Office connection therewith: Richard D. Fuerle, Registration No. 24,640		nd to transact all business in the Patent and
Address all telephone calls to: Richard D. J	Fuerle at telephone no. (7)	(6) 774-0091
Address all correspondence to:	Richard D. Fu	ıerle
	1711 West Ri	
	Grand Island,	NY 14072
tion and belief are believed to be true; and f statements and the like so made are punisha United States Code and that such willful fall issued thereon.	further that these statements ble by fine or imprisonments se statements may jeopardi	e are true and that all statements made on informa- s were made with the knowledge that willful false at, or both, under Section 1001 of Title 18 of the ze the validity of the application or any patent
Full name of sole inventor: YOSHIYUKI A	NDO 定藏 盖	4
	2 INC.	Date: December 3rd, 2003
Residence (City, State) Tokyo, Japan		Citizenship: <u>Japan</u>